Info Session 2:
Connecting the Dots Between CTE Career Clusters and CHIPS-Aligned Careers
Nov. 13, 2024 at 12:30PM ET

Logistics

- This event is being recorded.
- Recording and materials from this presentation will be available following the event on the challenge website.
- Closed captioning is provided.
- Put your questions in the Q&A section of Zoom. We will answer as many as we can at the end of the session.
- If you experience any technical difficulties, please send a message to the hosts and panelists via chat.

Agenda

- Challenge overview
- CHIPS and Science Act overview
- Panel discussion
- Next steps

CTE CHIPS Challenge Objectives

The purpose of the CTE CHIPS Challenge is to expand student recruitment, training, and placement strategies in semiconductor fabrication (fab) construction and advanced manufacturing careers that align with the CHIPS and Science Act (CHIPS).

The U.S. Department of Education invites entrants to submit innovative action plans to:

a) increase students' opportunities to develop skills and experiences that better prepare them for the careers aligned with CHIPS;

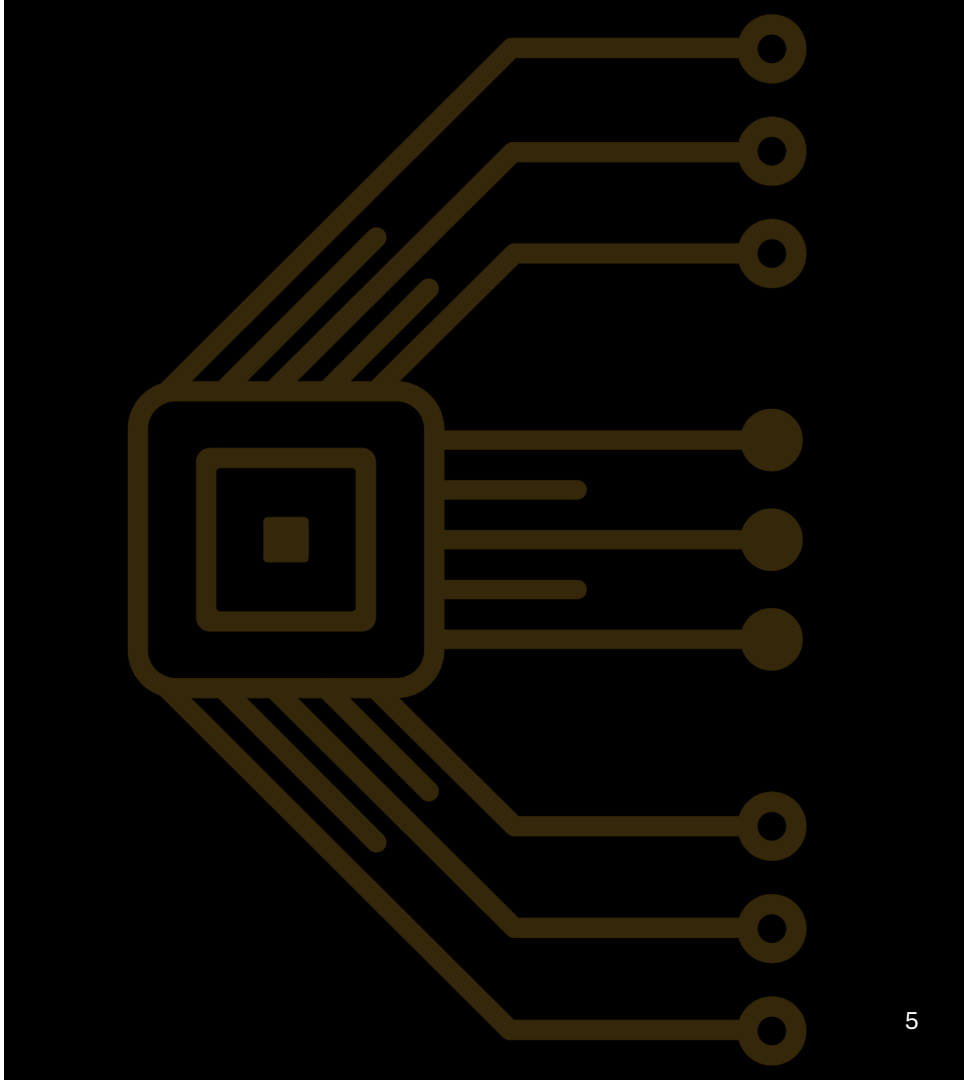
b) expand student recruitment, training, and placement strategies in these careers; and

c) support teachers in building out CTE programs.

Submissions are due Dec. 20, 2024 at 8PM ET.

Info Session 1 Resources

Webinar recording and slides are on the Resources page on CTECHIPSChallenge.com



CHIPS and Science Act of 2022 (CHIPS)

Fun Fact: CHIPS stands for "Creating Helpful Incentives to Produce Semiconductors"

The CHIPS and Science Act (CHIPS) is aimed at:

- Bolstering U.S. semiconductor capacity
- Solidifying U.S. leadership in scientific and technological innovation
- Catalyzing research and development (R&D)
- Creating regional high-tech hubs
- Fostering a bigger, more inclusive STEM workforce

Learn More: CHIPS and Science Act (CHIPS) and CHIPS Summary on Wikipedia

Additional Agency Resources

NSF	NIST	FEMI
U. S. National Science Foundation (NSF) NSF CHIPS and Science	U.S. Department of Commerce (DOC) National Institute of Standards and Technology (NIST) NIST CHIPS FOR AMERICA	U.S. Department of Energy (DOE) Foundation for Energy Security and Innovation (FESI)
Key technology areas • Advance manufacturing • Advanced materials • Artificial intelligence • Biotechnology • Communications and wireless • Cyberinfrastructure and advanced computing • Cybersecurity • Disaster risk and resilience • Energy technology • Quantum information science • Semiconductors and microelectronics	Topics • Electronics • Semiconductors • Manufacturing • Materials	Technology areas • Clean Hydrogen Liftoff • Advanced Nuclear • Long Duration Energy Storage • Carbon Management • Virtual Power Plants • Industrial Decarbonization Decarbonizing Chemicals and Refining • Low-Carbon Cement • Innovative Grid Deployment Next Generation Geothermal Power • Offshore Wind Liftoff • Electricity Demand Growth

View the full document: CHIPS-Aligned Careers: Sample Resources to Explore (accessible format) (PDF)

CHIPS Careers to CTE Program Examples

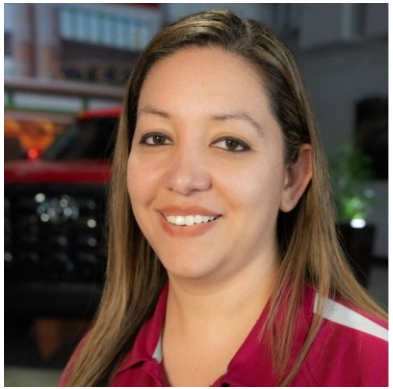
Semi-conductor Fab Construction	Advanced Manufacturing
• Architecture • Construction • HVAC • Electrical & Electronics	• Engineering • Robotics • Supply Chain Management • Machine Maintenance

Semi-conductor Fabrication	Bio Tech
• Software Development • Data Analytics • Technical Writing • Process Improvement	• Health Care • Artificial Intelligence & Machine Learning • Data Analytics • Ethics

Meet Our Panelists

Gena Gesing
Subject-Matter Expert
Kaptivate
(Panel Facilitator)

George Redmond
Founder
RadBio-Q



Monica Sabillon
HR Manager
Toyota North America

Mike Glavin
Program Director
Workforce Development
SEMI Foundation

Register for the Next Info Session and Office Hours

Info Session 3: How to Submit Your Action Plan

- Nov. 19, 2024 at 12:30 – 1:30PM ET
- Register here

Office Hours

- Dec. 11, 2024 at 12:30 – 1:30PM ET
- Dec. 16, 2024 at 12:30 – 1:30PM ET
- Register here

Thank you!

Join the community.

See you at our Info Session 3.
Contact us at hello@CTECHIPSChallenge.com.